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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/066,456	01/31/2002	Steven Teig	SPLX.P0099	2203	
23349	7590 09/03				
STATTLE	R JOHANSEN &	EXAMINER			
P O BOX 51 PALO ALTO	860 D, CA 94303		TAT, B	TAT, BINH C	
•	•		ART UNIT	PAPER NUMBER	
			2825		
				DATE MAILED: 00/03/2003	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)			
	10/066,456	TEIG ET AL.			
Office Action Summary	Examin r	Art Unit			
	Binh C. Tat	2825			
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply					
A SHORTENED STATUTORY PERIOD FOR RE THE MAILING DATE OF THIS COMMUNICATIO  - Extensions of time may be available under the provisions of 37 CFF after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a  - If NO period for reply is specified above, the maximum statutory per  - Failure to reply within the set or extended period for reply will, by sta  - Any reply received by the Office later than three months after the m earned patent term adjustment. See 37 CFR 1.704(b).  Status	N. R 1.136(a). In no event, however, may a least reply within the statutory minimum of this ide will apply and will expire SIX (6) MON atute, cause the application to become Al	reply be timely filed rty (30) days will be considered timely. NTHS from the mailing date of this communication. BANDONED (35 U.S.C. § 133).			
1) Responsive to communication(s) filed on _		•			
2a)☐ This action is <b>FINAL</b> . 2b)⊠	This action is non-final.				
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.  Disposition of Claims					
4)⊠ Claim(s) <u>1-18</u> is/are pending in the applica	tion				
	4a) Of the above claim(s) is/are withdrawn from consideration.				
<u> </u>					
5)  Claim(s) is/are allowed. 6)  Claim(s) <u>1-18</u> is/are rejected.					
7) Claim(s) is/are objected to.					
8) Claim(s) are subject to restriction and/or election requirement.  Application Papers					
9) The specification is objected to by the Examiner.					
10)⊠ The drawing(s) filed on <u>13 January 2002</u> is/are: a)⊠ accepted or b)□ objected to by the Examiner.					
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).					
11) The proposed drawing correction filed on is: a) approved b) disapproved by the Examiner.					
If approved, corrected drawings are required in reply to this Office action.					
12)☐ The oath or declaration is objected to by the Examiner.					
Priority under 35 U.S.C. §§ 119 and 120					
13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).					
a) ☐ All b) ☐ Some * c) ☐ None of:					
1.☐ Certified copies of the priority docum	ents have been received.				
2. Certified copies of the priority docum	ents have been received in A	Application No			
<ul> <li>3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).</li> <li>* See the attached detailed Office action for a list of the certified copies not received.</li> </ul>					
14)☐ Acknowledgment is made of a claim for dome	estic priority under 35 U.S.C.	§ 119(e) (to a provisional application).			
a) The translation of the foreign language provisional application has been received.  15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.					
Attachment(s)					
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449) Paper Notice	5) Notice of	Summary (PTO-413) Paper No(s) Informal Patent Application (PTO-152)			
U.S. Patent and Trademark Office PTO-326 (Rev. 04-01) Office	Action Summary	Part of Paper No. 4			

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## **DETAILED ACTION**

This office action is in response to application 10/066456 filed on 01/31/02.
 Claims 1-14 remain pending in the application.

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## Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.
- 2. The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) do not apply to the examination of this application as the application being examined was not (1) filed on or after November 29, 2000, or (2) voluntarily published under 35 U.S.C. 122(b). Therefore, this application is examined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).
- 3. Claims 1-18 are rejected under 35 U.S.C. 102(e) as being anticipated by Pederson et al. (U.S. 6490717).
- 4. As to claims 1 (method) and 15 (computer program) Pederson et al. teach a method for performing technology mapping, the method comprising: a) receiving a design that is not bounded to a particular technology (see col 8 lines 16-30 and fig 8); b) repeatedly: selecting from the design a candidate sub-network, identifying at least one replacement sub-network from a storage structure that stores replacement sub-networks (see fig 3a and 3b col 9 lines 51-67 and col 10, 11 and fig 8); replacing the selected candidate sub-network in the design with the

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replacement sub-network (see fig 3a and 3b col 9 lines 51-67 and col 10, 11 and summary and fig 8); c) wherein at least each of a first set of replacement sub-networks have multiple circuit elements, wherein each of the multiple circuit elements of a first-set replacement subnetwork is independently selectable by the method (see fig 3a, 3b, 7a-d col 9 lines 51-67 and col 10, 11, 14).

- 5. As to claims 2-5 Pederson et al. teach wherein identifying the replacement sub-network comprises: generating a parameter based on a set of output functions performed by the selected candidate sub-network, wherein the parameter identifies the replacement sub-network (see fig 3a and 3b col 9 lines 51-67 and col 10, 11).
- 6. As to claims 6-7 and 16-18 Pederson et al. teach terminating the repetitions once a stopping criteria is reached (see fig 3a and 3b col 9 lines 51-67 and col 10, 11 and fig 8); and wherein the design includes a plurality of circuit elements and the sub-networks are formed by circuit elements, the method further comprising: after terminating the repetitions, traversing the design to identify circuit elements that are not bound to the technology library (see fig 3a and 3b col 9 lines 51-67 and col 10, 11 and fig 8); for each identified circuit element, attempting to identify a replacement sub-network that is stored in the storage structure (see fig 3a and 3b col 9 lines 51-67 and col 10, 11 and fig 8); if at least one replacement sub-network for an identified circuit element is identified, replacing the circuit element in the design with the identified replacement sub-network (see fig 3a and 3b col 9 lines 51-67 and col 10, 11 and fig 8).
- 7. As to claims 8-9 Pederson et al. teach wherein if more than one replacement sub-networks are identified for a circuit element, selecting one of the replacement sub-networks and replacing the circuit element with the selected replacement sub-network (see fig 3a and 3b col 9 lines 51-67 and col 10, 11).

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8. As to claim 10 Pederson et al. teach wherein each circuit element performs a function, wherein if no replacement sub-network is identified for an identified circuit element, decomposing the function of the circuit element into a set of functions, and then attempting to identify a set of replacement sub-networks in the storage structure that perform the set of functions (see fig 3a and 3b col 9 lines 51-67 and col 10, 11).

- 9. As to claims 11-12 Pederson et al. teach wherein traversing the design to identify circuit elements comprises identifying circuit elements that existed in the design when the design was received; after traversing the design, repeatedly: selecting from the design a candidate sub-network, identifying at least one replacement sub-network from a storage structure that stores replacement sub-networks, replacing the selected candidate sub-network in the design with the replacement sub-network (see fig 3a and 3b col 9 lines 51-67 and col 10, 11).
- 10. As to claims 13-14 Pederson et al. teach before replacing the candidate sub-networks with the replacement sub-networks, evaluating whether to replace the selected candidate sub-network with the replacement sub network, wherein certain candidate sub-networks are replaced by replacement sub-networks based on the evaluation, wherein certain candidate sub-networks are not replaced based on the evaluations (see fig 3a and 3b col 9 lines 51-67 and col 10, 11).

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## Conclusion

11. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Binh C. Tat whose telephone number is (703) 305-4855. The examiner can normally be reached on 7:30 - 4:00 (M-F).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mathew Smith can be reached on (703) 308-1323. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 305-3431 for regular communications and (703) 305-3431 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-1782.

Binh Tat Art Unit 2825 June 13, 2003

> VUTHE SIER PRIMARY EXAMINATION